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Date : August 25, 2006
TO : Director, Office of Patent Publication
FROM : Office of the Deputy Commissioner
for Patent Examination Policy
SUBJECT : Withdrawal from Issue of

Applicant(s) : Richard K. Williams, et al.
Application No. : 10/767,751
Filed : January 28, 2004


The above-identified application has been assigned Patent No. 7,105,890 and an issue date of September 12, 2006.

It is hereby directed that this application be withdrawn from issue at the request of the applicant.

Do not refund the issue fee.

The following erratum should be published in the Official Gazette if the above-identified application is published in the OG of September 12, 2006:

"All reference to Patent No. 7,105,890 to RICHARD K. WILLIAMS, ET AL. of CALIFORNIA for MODULAR BIPOLAR-CMOS-DMOS ANALOG INTEGRATED CIRCUIT AND POWER TRANSISTOR TECHNOLOGY appearing in the Official Gazette of September 12, 2006, should be deleted since no patent was granted."


Sherry D. Brinkley
Petitions Examiner
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